RSPD-10GBD80-4955/RSPD-10GBD-5549

10Gb/s 80km BiDi SFP+ Transceiver Hot Pluggable, Single LC, +3.3V,

1490/1550nm 1550/1490nm EML&APD, Single mode

Features:

- Supports 9.95 to 11.3Gb/s bit rates
- Hot-Pluggable
- Single LC for Bi-directional Transmission
- cooled EML transmitter, APD photo-detector
- SMF links up to 80km
- 2-wire interface for management specifications compliant with SFF 8472 digital diagnostic monitoring interface
- Power Supply :+3.3V
- Power consumption<3W
- Temperature Range: 0~ 70°C
- RoHS compliant

Applications:

- 10GBASE-ZR/ZW Ethernet
- SONET OC-192 / SDH
- 10G Fibre channel

Description:

FIBERWDM's RSPD-10GBD80-4955/RSPD-10GBD80-5549 is a very compact 10Gb/s optical transceiver module for serial optical communication applications at 10Gb/s. The RSPD-10GBD80-4955 & RSPD-10GBD80-5549 converts a 10Gb/s serial electrical data stream to 10Gb/s optical output signal and a 10Gb/s optical input signal to 10Gb/s serial electrical data



streams. The high speed 10Gb/s electrical interface is fully compliant with SFI specification.

The high performance 1490/1550nm 1550/1490nm cooled EML transmitter and high sensitivity APD receiver provide superior performance for Ethernet applications at up to 80km links.

The SFP+ Module compliants with SFF-8431, SFF-8432 and IEEE 802.3ae 10GBASE-ZR. Digital diagnostics functions are available via a 2-wire serial interface, as specified in SFF-8472.

The fully SFP compliant form factor provides hot plug-ability, easy optical port upgrades and low EMI emission.

Absolute Maximum Ratings

Parameter	Symbol	Min.	Typical	Max.	Unit
Storage Temperature	Ts	-40		+85	°C
Case Operating Temperature	T _A	0		70	°C
Maximum Supply Voltage	Vcc	-0.5		4	V
Relative Humidity	RH	0		85	%

• Electrical Characteristics (T_{OP} = 0 to 70 °C, VCC = 3.135 to 3.465 Volts)

Parameter	Symbol	Min.	Typical	Max.	Unit	Note
Supply Voltage	Vcc	3.135		3.465	V	
Supply Current	lcc			500	mA	
Power Consumption	Р			1.8	W	
Transmitter Section:						
Input differential impedance	R _{in}		100		Ω	1
Tx Input Single Ended DC Voltage Tolerance (Ref VeeT)	V	-0.3		4	V	
Differential input voltage swing	Vin,pp	180		700	mV	2
Transmit Disable Voltage	VD	2		Vcc	V	3
Transmit Enable Voltage	V _{EN}	Vee		Vee+0.8	V	
Receiver Section:						
Single Ended Output Voltage Tolerance	V	-0.3		4	V	
Rx Output Diff Voltage	Vo	300		850	mV	
Rx Output Rise and Fall Time	Tr/Tf	30			ps	4



LOS Fault	$V_{LOSfault}$	2	Vcc _{HOST}	V	5
LOS Normal	$V_{LOS norm}$	Vee	Vee+0.8	V	5

Note:

- 1. Connected directly to TX data input pins. AC coupling from pins into laser driver IC.
- 2. Per SFF-8431 Rev 3.0
- 3. Into 100 ohms differential termination.
- 4. 20%~80%
- 5. LOS is an open collector output. Should be pulled up with $4.7k 10k\Omega$ on the host board. Normal operation is logic 0; loss of signal is logic 1. Maximum pull-up voltage is 5.5V.

Optical Parameters(T_{OP} = 0 to 70°C, VCC = 3.135 to 3.465 Volts)

Pa	Symbol	Min.	Typical	Max.	Unit	Note	
Transmitter S	ection:						
Center	RSPD-10GBD80-4955	λt		1490		nm	
Wavelength	RSPD-10GBD80-5549	λt		1550		nm	
spectral width	1	Δλ			0.3	nm	
Average Optic	al Power	Pavg	0		+5	dBm	1
Optical Power	OMA	Poma	-2.1			dBm	
Laser Off Pow	er	Poff			-30	dBm	
Extinction Rat	io	ER	8.2			dB	
Transmitter D	ispersion Penalty	TDP			3.0	dB	2
Relative Inten	sity Noise	Rin			-128	dB/Hz	3
Optical Returr	Loss Tolerance		21			dB	
Receiver Section:							
Center	RSPD-10GBD80-4955	λr		1490		nm	
Wavelength	RSPD-10GBD80-5549	λr		1550		nm	
Receiver Sensitivity		Sen			-23	dBm	4
Los Assert		LOSA	-34		-	dBm	
Los Dessert		LOS _D			-24	dBm	
Los Hysteresis	Los Hysteresis		0.5			dB	
Overload		Sat	-7			dBm	5
Receiver Refle	ectance	Rrx			-26	dB	

Note:

- 1. Average power figures are informative only, per IEEE802.3ae.
- 2. TWDP figure requires the host board to be SFF-8431compliant. TWDP is calculated using the Matlab code provided in clause 68.6.6.2 of IEEE802.3ae.
- 3. 12dB reflection.
- 4. Conditions of stressed receiver tests per IEEE802.3ae. CSRS testing requires the host board to be SFF-8431 compliant.

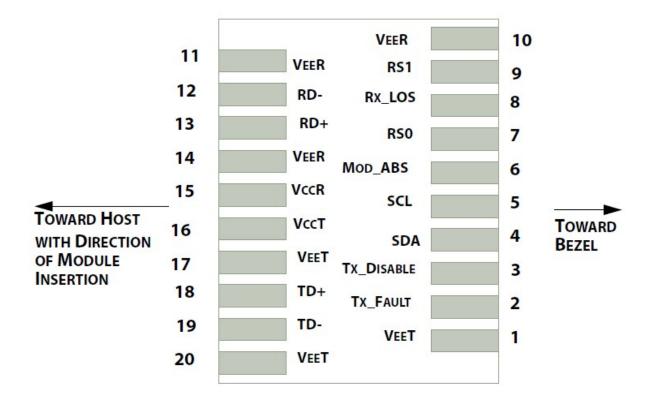
5. Receiver overload specified in OMA and under the worst comprehensive stressed condition.

• Timing Characteristics

Parameter	Symbol	Min.	Typical	Max.	Unit
TX_Disable Assert Time	t_off			10	us
TX_Disable Negate Time	t_on			1	ms
Time to Initialize Include Reset of TX_FAULT	t_int			300	ms
TX_FAULT from Fault to Assertion	t_fault			100	us
TX_Disable Time to Start Reset	t_reset	10			us
Receiver Loss of Signal Assert Time	T _A ,RX_LOS			100	us
Receiver Loss of Signal Deassert Time	T _d ,RX_LOS			100	us
Rate-Select Chage Time	t_ratesel			10	us
Serial ID Clock Time	t_serial-clock			100	kHz

• Pin Assignment

Diagram of Host Board Connector Block Pin Numbers and Name



• Pin Function Definitions

FIBER WD M

PIN #	Name	Function	Notes
1	VeeT	Module transmitter ground	1
2	Tx Fault	Module transmitter fault	2
3	Tx Disable	Transmitter Disable; Turns off transmitter laser output	3
4	SDL	2 wire serial interface data input/output (SDA)	
5	SCL	2 wire serial interface clock input (SCL)	
6	MOD-ABS	Module Absent, connect to VeeR or VeeT in the module	2
7	RSO	Rate select0, optionally control SFP+ receiver. When high, input data rate >4.5Gb/ s; when low, input data rate <=4.5Gb/s	
8	LOS	Receiver Loss of Signal Indication	4
9	RS1	Rate select0, optionally control SFP+ transmitter. When high, input data rate >4.5Gb/s; when low, input data rate <=4.5Gb/s	
10	VeeR	Module receiver ground	1
11	VeeR	Module receiver ground	1
12	RD-	Receiver inverted data out put	
13	RD+	Receiver non-inverted data out put	
14	VeeR	Module receiver ground	1
15	VccR	Module receiver 3.3V supply	
16	VccT	Module transmitter 3.3V supply	
17	VeeT	Module transmitter ground	1
18	TD+	Transmitter inverted data out put	
19	TD-	Transmitter non-inverted data out put	
20	VeeT	Module transmitter ground	1

Note:

- 1. The module ground pins shall be isolated from the module case.
- 2. This pin is an open collector/drain output pin and shall be pulled up with 4.7K-10Kohms to Host_Vcc on the host board.
- 3. This pin shall be pulled up with 4.7K-10Kohms to VccT in the module.
- 4. This pin is an open collector/drain output pin and shall be pulled up with 4.7K-10Kohms to Host_Vcc on the host board.

• SFP Module EEPROM Information and Management

The SFP modules implement the 2-wire serial communication protocol as defined in the SFP -8472. The serial ID information of the SFP modules and Digital Diagnostic Monitor parameters can be accessed through the I²C interface at address A0h and A2h. The memory is mapped in Table 1. Detailed ID information (A0h) is listed in Table 2. And the DDM specification at address A2h. For more details of the memory map and byte definitions, please refer to the SFF-8472, "Digital Diagnostic Monitoring Interface for Optical Transceivers". The DDM parameters have been internally calibrated.



Table 1. Digital Diagnostic Memory Map (Specific Data Field Descriptions)

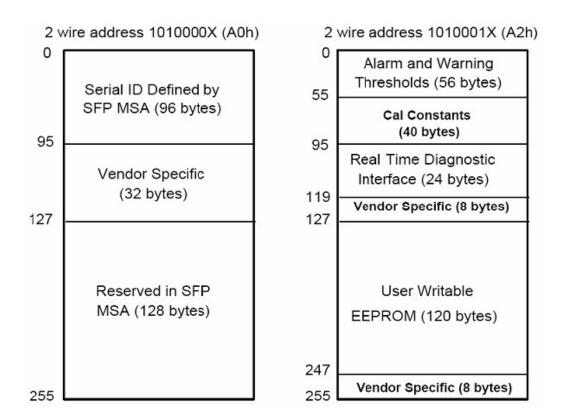


	Table 2 - EEPROM Serial ID Memor	v Contents (A0h)
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Data	Length	Name of	Description and Contents
Address	(Byte)	Length	Description and contents
Base ID Fiel	ds		
0	1	Identifier	Type of Serial transceiver (03h=SFP)
1	1	Reserved	Extended identifier of type serial transceiver (04h)
2	1	Connector	Code of optical connector type (07=LC)
3-10	8	Transceiver	10G Base-ZR
11	1	Encoding	64B/66B
12	1	BR, Nominal	Nominal baud rate, unit of 100Mbps
13-14	2	Reserved	(0000h)
15	1	Length(9um)	Link length supported for 9/125um fiber, units of 100m
16	1	Length(50um)	Link length supported for 50/125um fiber, units of 10m
17	1	Length(62.5um)	Link length supported for 62.5/125um fiber, units of
			10m
18	1	Length(Copper)	Link length supported for copper, units of meters
19	1	Reserved	
20-35	16	Vendor Name	SFP vendor name: FIBERWDM
36	1	Reserved	



3	Vendor OUI	SFP transceiver vendor OUI ID			
16	Vendor PN	Part Number: "RSPD-10GBD80-4955 or			
		RSPD-10GBD80-5549" (ASCII)			
4	Vendor rev	Revision level for part number			
3	Reserved				
1	CCID	Least significant byte of sum of data in address 0-62			
Fields					
2	Option	Indicates which optical SFP signals are implemented			
		(001Ah = LOS, TX_FAULT, TX_DISABLE all supported)			
1	BR, max	Upper bit rate margin, units of %			
1	BR, min	Lower bit rate margin, units of %			
16	Vendor SN	Serial number (ASCII)			
8	Date code	FIBERWDM's Manufacturing date code			
3	Reserved				
1	CCEX	Check code for the extended ID Fields (addresses 64 to			
		94)			
Vendor Specific ID Fields					
32	Readable	FIBERWDM specific date, read only			
128	Reserved	Reserved for SFF-8079			
	4 3 1 Fields 2 1 1 16 8 3 1 :ific ID Fields 32	16Vendor PN4Vendor rev3Reserved1CCIDFields22Option1BR, max1BR, min16Vendor SN8Date code3Reserved1CCEXcific ID Fields32			

• Digital Diagnostic Monitor Characteristics

Data Address	Parameter	Accuracy	Unit
96-97	Transceiver Internal Temperature	±3.0	°C
98-99	VCC3 Internal Supply Voltage	±3.0	%
100-101	Laser Bias Current	±10	%
102-103	Tx Output Power	±3.0	dB
104-105	Rx Input Power	±3.0	dB

• Regulatory Compliance

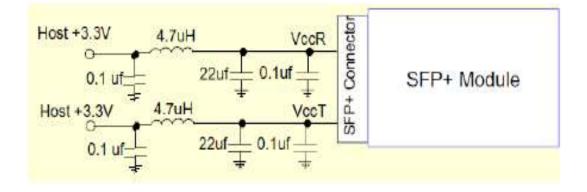
The TSBXG80D-XX complies with international Electromagnetic Compatibility (EMC) and international safety requirements and standards (see details in Table following).

Electrostatic Discharge	MIL-STD-883E	Class 1(>1000 V)
(ESD) to the Electrical Pins	Method 3015.7	
Electrostatic Discharge (ESD)	IEC 61000-4-2	Compatible with standards
to the Single LC Receptacle	GR-1089-CORE	
Electromagnetic	FCC Part 15 Class B	Compatible with standards
Interference (EMI)	EN55022 Class B (CISPR 22B)	

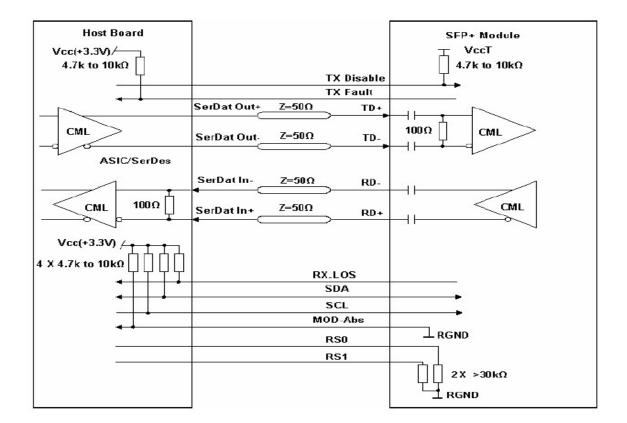


	VCCI Class B	
Laser Eye Safety	FDA 21CFR 1040.10 and 1040.11	Compatible with Class 1 laser
	EN60950, EN (IEC) 60825-1,2	product.

• Recommended Circuit



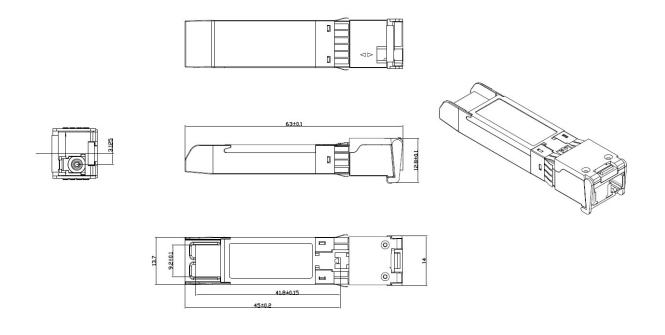
Recommended Host Board Power Supply Circuit



Recommended High-speed Interface Circuit



Mechanical Dimensions



Ordering Information

Part Number	Transmitter	Output Power	Receiver	Sensitivity	Reach	Temp	DDM	RoHS
RSFD-10G-BD80-4955	1490nm EML	$0 \sim +5 dBm$	1550nm APD	< -23dBm	80km	$0\sim 70^{o}C$	Available	Compliant
RSFD-10G-BD80-5549	1550nm EML	$0 \sim +5 dBm$	1490nm APD	< -23dBm	80km	$0\sim 70^{o}C$	Available	Compliant

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